

FIG. 1 is a block diagram of a system architecture.

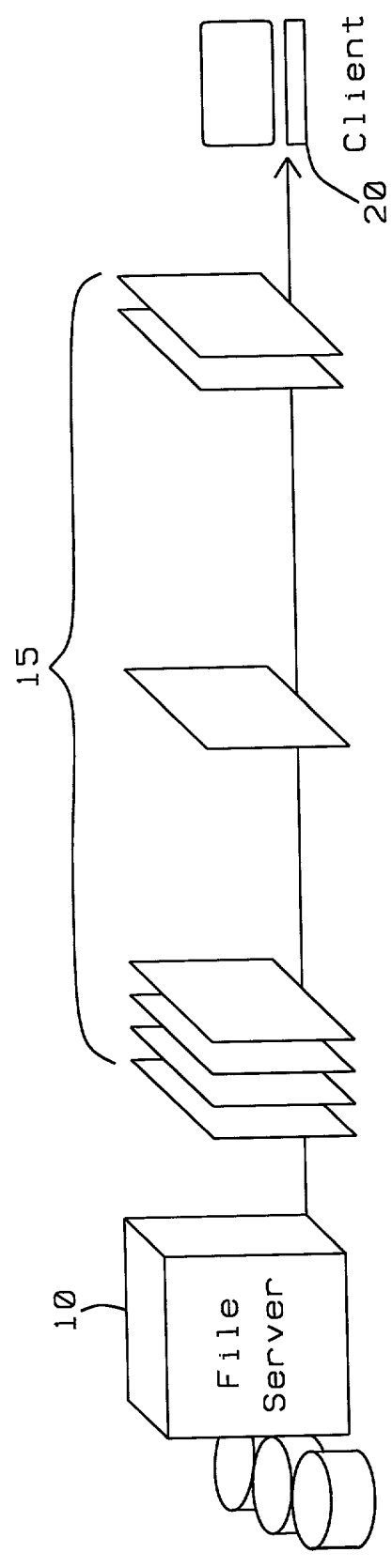


FIG. 1 - Prior Art

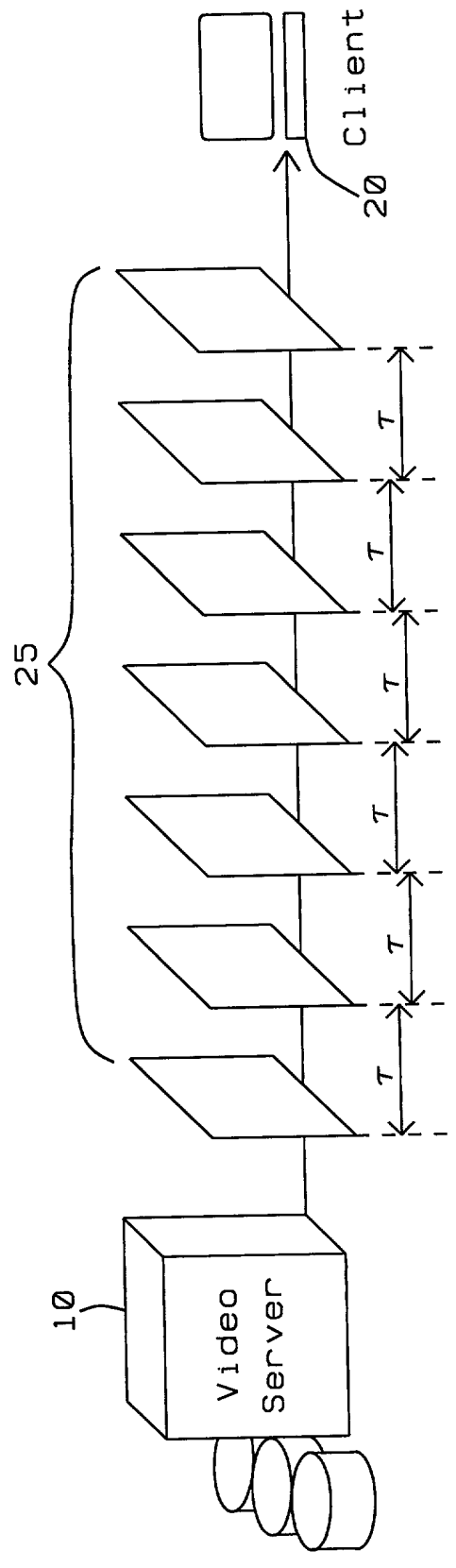
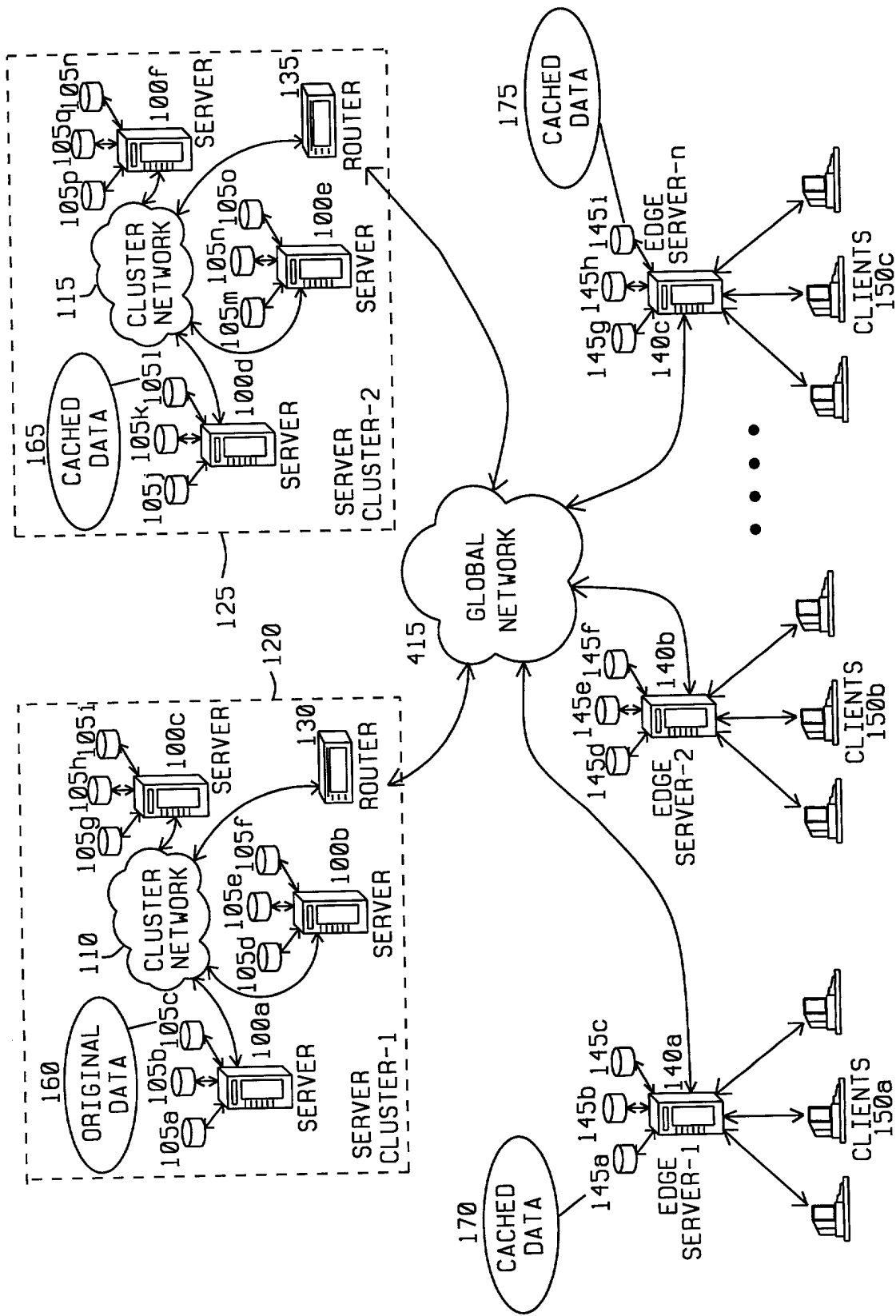


FIG. 2 - Prior Art



*FIG. 3 - Prior Art*

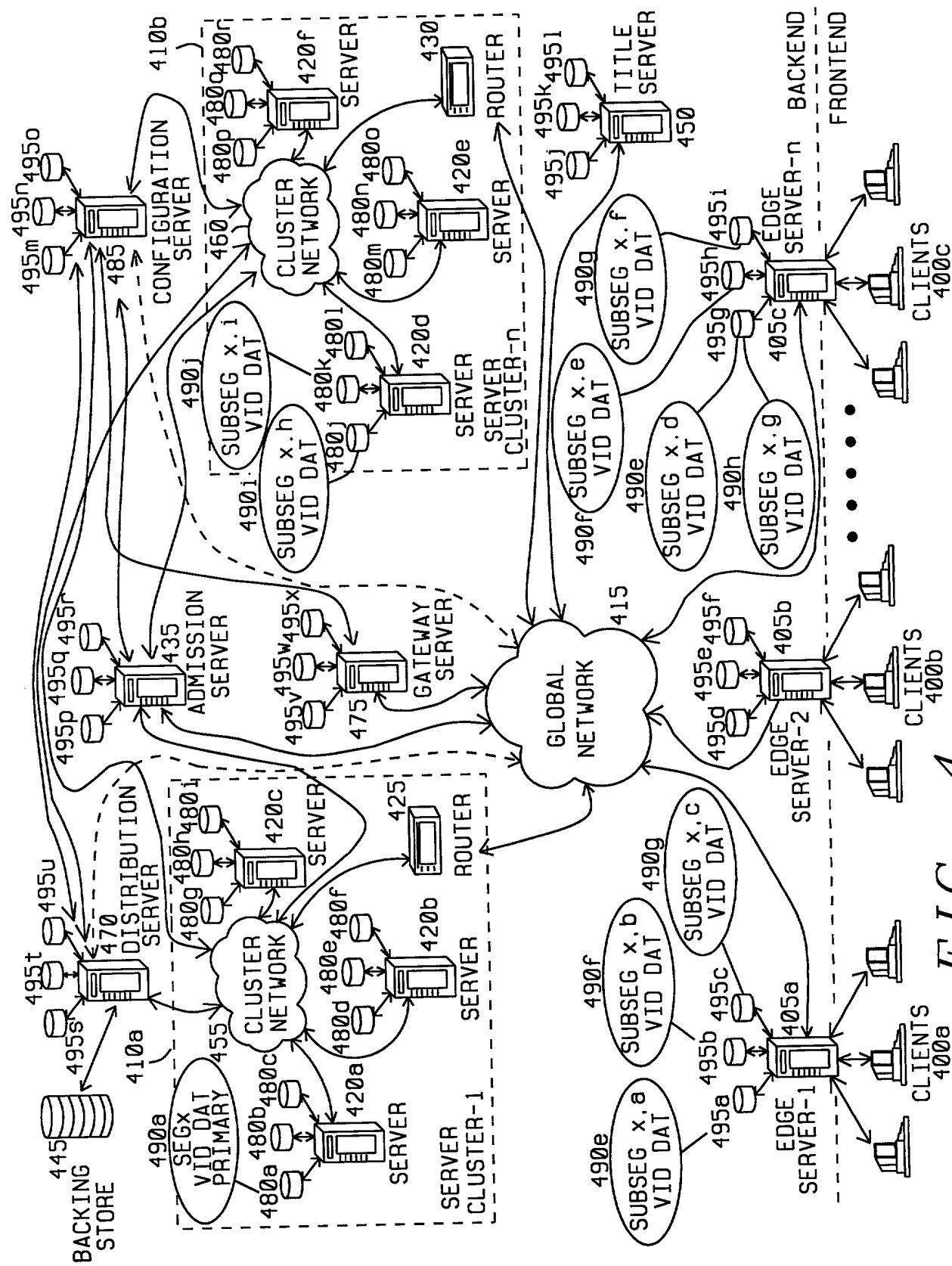


FIG. 4

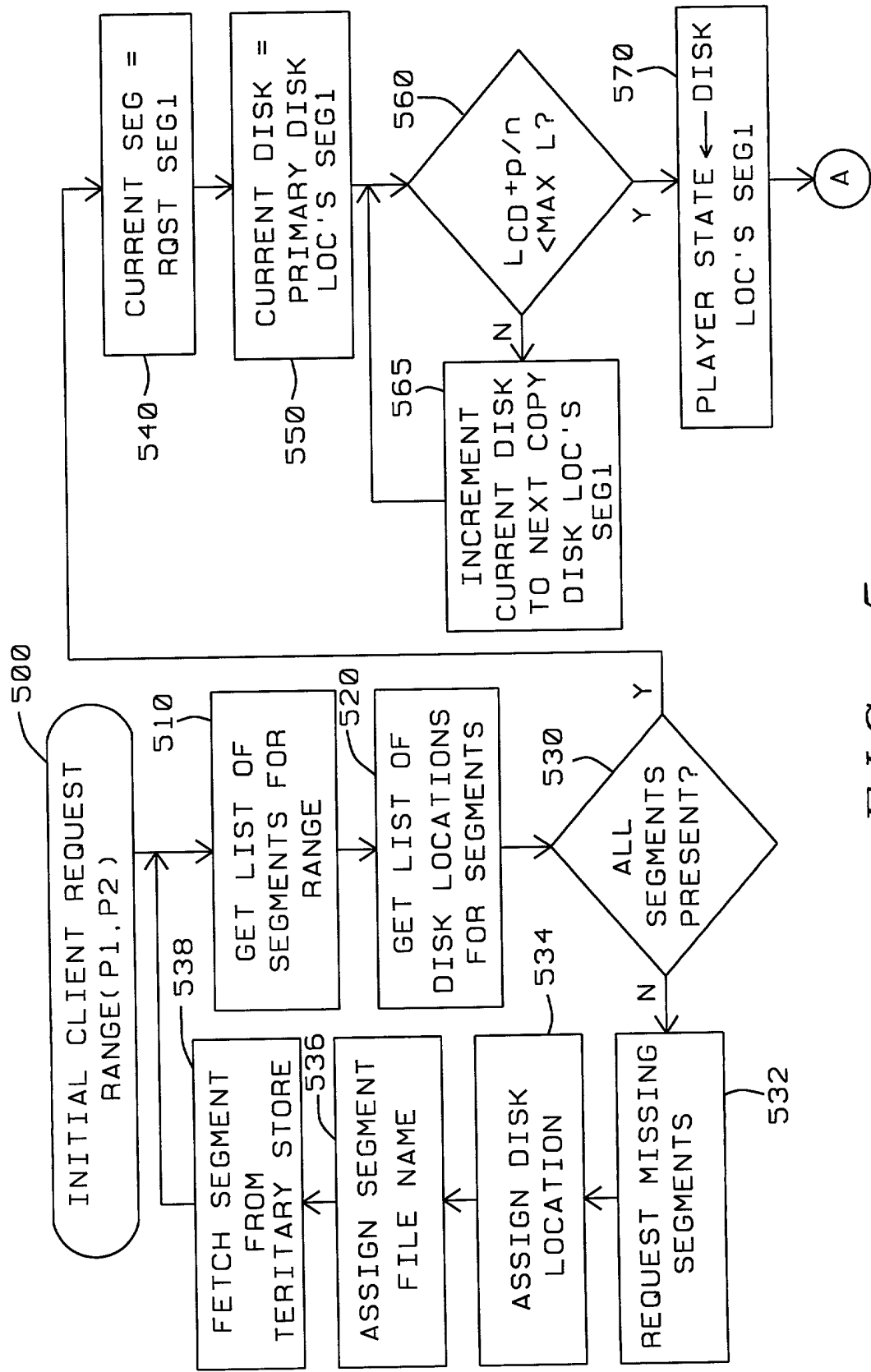


FIG. 5

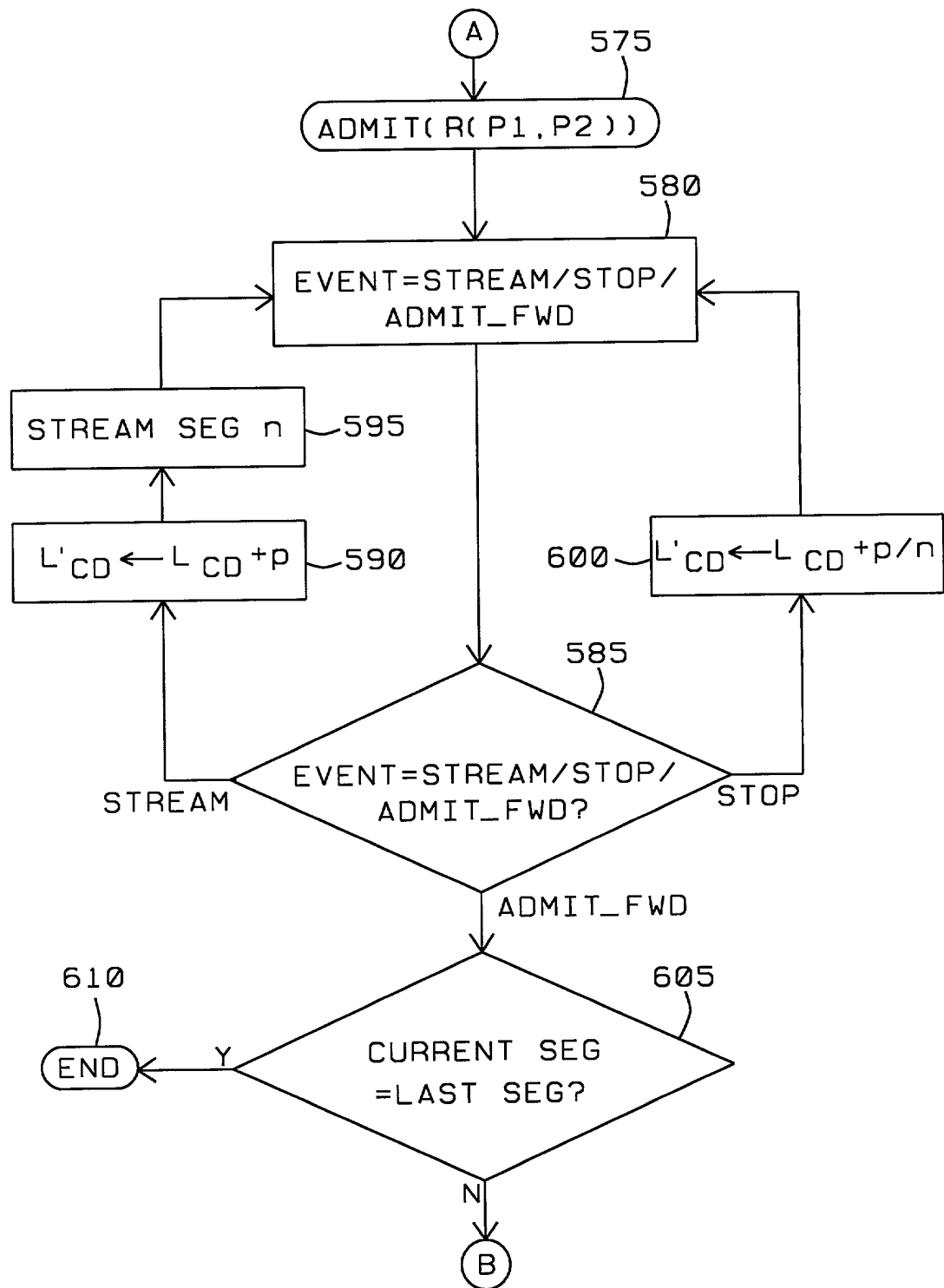


FIG. 6

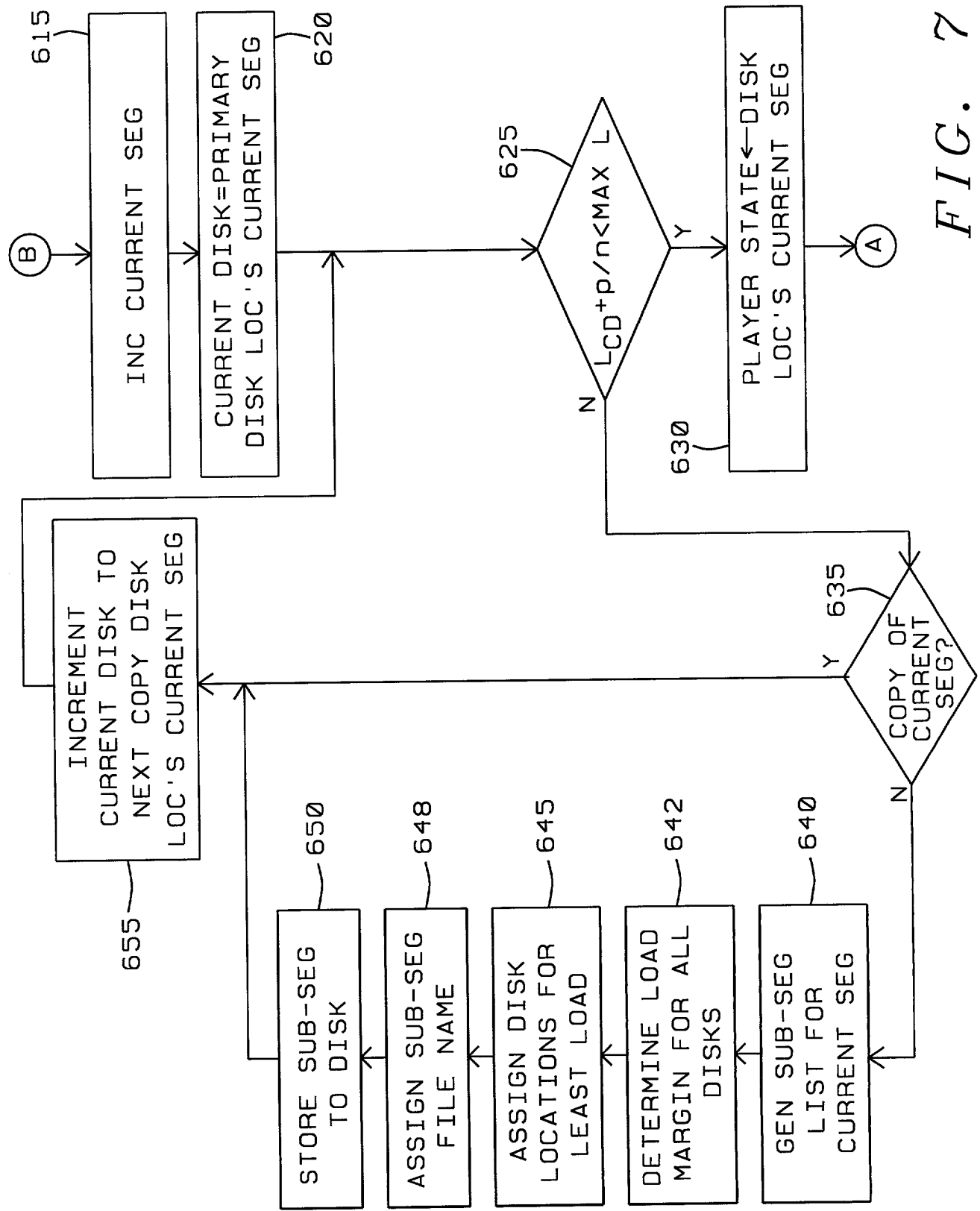


FIG. 7

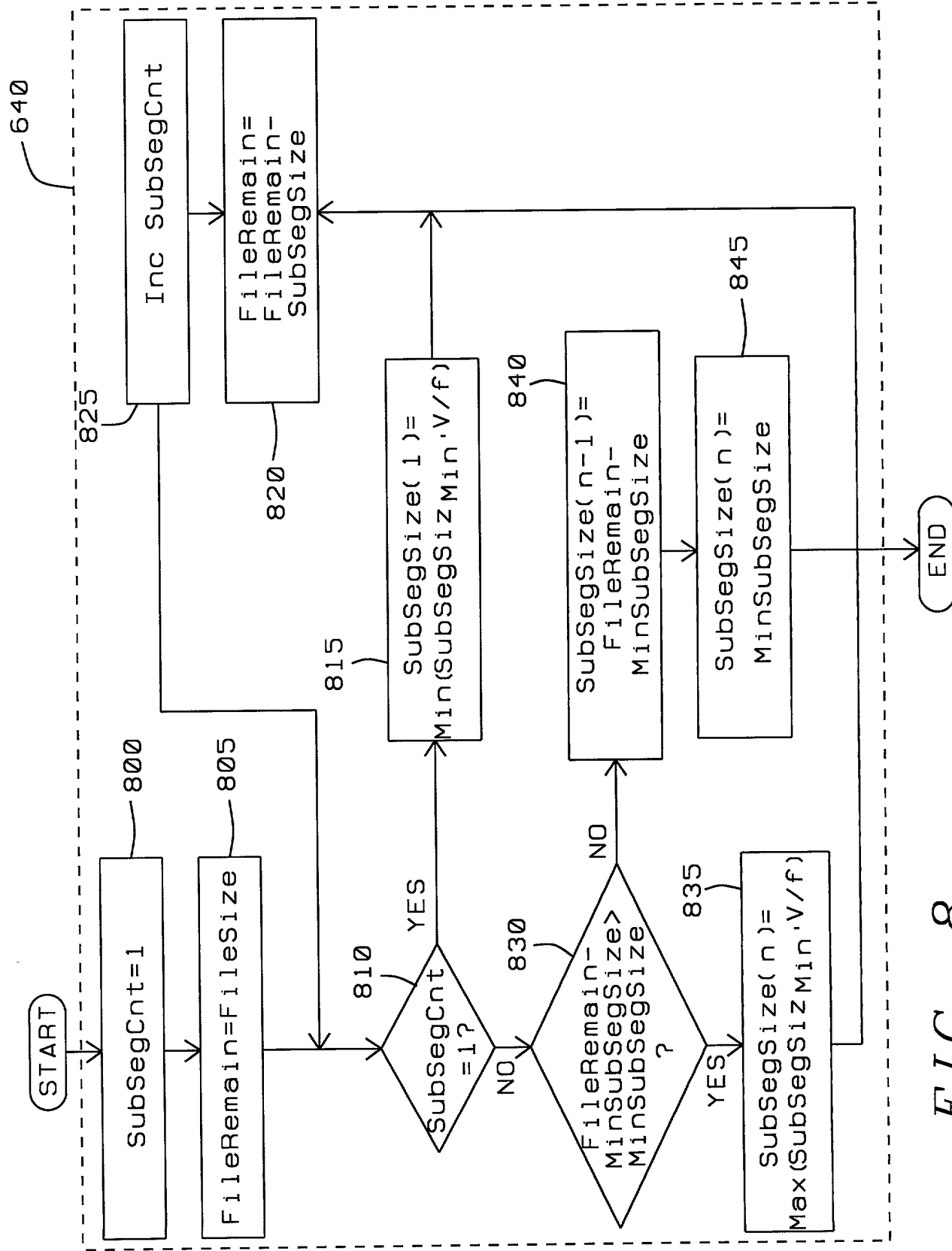


FIG. 8